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Our File No. 9281-4731  
Client Reference No. S US02275

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of: )  
Yoshitaka Hirose )  
Serial No. To Be Assigned )  
Filing Date: Herewith )  
For: Compact Circuit Module Having High )  
Mounting Accuracy And Method Of )  
Manufacturing The Same )

**INFORMATION DISCLOSURE STATEMENT**

Mail Stop Patent Application  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

Pursuant to the obligation under 37 C.F.R. 1.56 and in conformance with 37 C.F.R. 1.97-1.99, Applicant hereby submits references A1-A2 listed on the attached form PTO-1449 for consideration by the Examiner. Copies of the references are enclosed herewith.

Japanese Unexamined Patent Application Publication Nos. 2001-156110 and 2001-060602 disclose a mounting method of a semiconductor chip for attaching a flip-chip component.

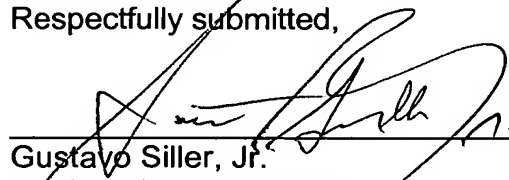
The filing of this Information Disclosure Statement does not constitute an admission that the information cited herein is, or is considered to be, material to patentability as defined in 37 C.F.R. Section 1.56(b). Further, Applicant reserves the right to contest these references as prior art against the present application, and Applicant does not believe that the disclosure of these references, even if finally determined to be prior art, anticipates Applicant's invention or that these references make Applicant's invention obvious.

No fees are believed to be due in connection with filing of this Information Disclosure Statement, however, should any fees under 37 C.F.R. §§ 1.16 to 1.21 be

deemed necessary for any reason relating to this material, the Commissioner is hereby authorized to deduct said fees from Brinks Hofer Gilson & Lione Deposit Account No. 23-1925.

Applicant respectfully requests that the Examiner review the entire disclosure of these documents and make them of record.

Respectfully submitted,



Gustavo Siller, Jr.

Registration No. 32,305

Attorney for Applicant

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<b>FORM PTO-1449</b>  <b>LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT</b>  (use several sheets if necessary)	SERIAL NO.	CASE NO.
	To be Assigned	9281-4731
	FILING DATE	GROUP ART UNIT
	Herewith.	
APPLICANT(S): Yoshitaka Hirose		

**REFERENCE DESIGNATION**
**U.S. PATENT DOCUMENTS**

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS/ SUBCLASS	FILING DATE

**FOREIGN PATENT DOCUMENTS**

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS/ SUBCLASS	TRANSLATION YES	NO
	A1	2001-156110	06/2001	JP		X	
	A2	2001-060602	03/2001	JP		X	

EXAMINER INITIAL	OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)	

EXAMINER	DATE CONSIDERED
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Rev. Oct. 97